

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1

Complete If Known	
Application Number	10/676,883
Filing Date	9/30/2003
First Named Inventor	Christopher L. Rumer
Art Unit	2825
Examiner Name	C. Everhart
Attorney Docket Number	42P16840

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
<i>CYME</i>		RECHE, J., "Wafer Thinning and Thru-Silicon Vias", The Path to Wafer Level Packaging, 41 pages, Tru-Si Technologies, www.trusi.com , IEEE/CPMT Meeting, Santa Clara (CA)	
		05/10/2000.	

**Examiner
Signature**

C. Eerhart

Date
Considered

11-12-2004

**Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

**Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.*

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
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